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**AMENDMENTS TO THE CLAIMS**

Please amend the claims of the present application as set forth below. In accordance with the PTO's revised amendment format, a detailed listing of all claims has been provided. This listing of claims will replace all prior versions, and listings, of claims in the application.

By way of overview, claims 1-5, 9-27 and 31-61 are now currently pending (because claims 6-8 and 28-30). Of these pending claims:

- a) Claims 2, 3, 5, 9-22, 24-27, 31-43 and 45 remain in their original form;
- b) Claims 1, 23, 44, 52, 53, 55, 57, 58 and 60 are currently amended;
- c) Claim 4 is previously amended; and
- d) Claims 46-51, 54, 56, 59 and 61 were previously added.

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**Listing of Claims**

1. (Currently Amended) A video output system for producing video signals within a video graphics workstation, the video output system comprising:

a receiver for receiving a video signal forwarded from a video signal source within the video graphics workstation;

a video pipeline for post-processing the received video signal, the video pipeline producing a post-processed video signal; and

a video output module for converting the post-processed video signal, the video output module producing a formatted video signal,

wherein the video output system is ~~selectively coupled to~~ configured to receive the received video signal from a storage medium as one video signal source, a video graphics processor, and a video input system, wherein the video output system is

1 communicatively coupled to the storage medium, the graphics processor, and the video  
2 input system by electrical communication paths.

3 ~~wherein the video output system is selectively coupled to a video graphics~~  
4 ~~processor as another video signal source, and~~

5 ~~wherein the video output system is selectively coupled to a video input system as~~  
6 ~~another video signal source.~~

7  
8 2. (Original) The video output system according to claim 1 wherein the video  
9 output module further comprises:

10 an ancillary data injector, the injector inserting ancillary data into the  
11 post-processed video signal.

12  
13 3. (Original) The video output system according to claim 1, further comprising:  
14 a generator locking device.

15  
16 4. (Previously Amended) The video output system according to claim 1 wherein  
17 the video output module includes a generator locking device.

18  
19 5. (Original) The video output system according to claim 1 wherein the received  
20 video signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded  
21 video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.

22  
23 6. (Cancelled)

24  
25 7. (Cancelled)

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2 8. (Cancelled)

3  
4 9. (Original) The video output system according to claim 1 wherein the formatted  
5 video signal is VS, wherein VS is an analog composite video signal, an analog  
6 component video signal, a serial digital composite video signal, a serial digital  
7 component video signal, a parallel digital composite video signal, or a parallel digital  
8 component video signal.

9  
10 10. (Original) The video output system according to claim 1 wherein the process  
11 of post-processing includes region of interest selection.

12  
13 11. (Original) The video output system according to claim 1 wherein the process  
14 of post-processing includes frame rate matching.

15  
16 12. (Original) The video output system according to claim 1 wherein the process  
17 of post-processing includes spatial adaptation.

18  
19 13. (Original) The video output system according to claim 12 wherein the process  
20 of spatial adaptation includes scaling.

21  
22 14. (Original) The video output system according to claim 12 wherein the process  
23 of spatial adaptation includes picture framing.

1 15. (Original) The video output system according to claim 14 wherein the process  
2 of picture framing includes letter boxing.  
3

4 16. (Original) The video output system according to claim 1 wherein the process  
5 of post-processing includes changing the sample rate of the video signal being  
6 post-processed.  
7

8 17. (Original) The video output system according to claim 1 wherein the process  
9 of post-processing includes gamma removal.  
10

11 18. (Original) The video output system according to claim 1 wherein the process  
12 of post-processing includes gamma insertion.  
13

14 19. (Original) The video output system according to claim 1 wherein the process  
15 of post-processing includes color space conversion.  
16

17 20. (Original) The video output system according to claim 1 wherein the process  
18 of post-processing includes changing frames of video data into interleaved fields of video  
19 data.  
20

21 21. (Original) The video output system according to claim 1 wherein the process  
22 of post-processing includes addressing on a frame-by-frame basis the video signal being  
23 post-processed.  
24  
25

1 22. (Original) The video output system according to claim 1 wherein the system is  
2 a Peripheral Component Interconnect circuit board.

3  
4 23. (Currently Amended) A method for producing video signals using a video  
5 output system within a video graphics workstation, the method comprising:

6 receiving a video signal in a receiver of the video output system, wherein the  
7 video signal is forwarded from a video signal source within the video graphics  
8 workstation;

9 post-processing the received video signal through a video pipeline of the video  
10 output system, producing a post-processed video signal; and

11 converting the post-processed video signal in a video output module of the video  
12 output system, producing a formatted video signal,

13 wherein the video output system is ~~selectively coupled to~~ configured to receive  
14 the received video signal from a storage medium as one video signal source, a video  
15 graphics processor, and a video input system, wherein the video output system is  
16 communicatively coupled to the storage medium, the graphics processor, and the video  
17 input system by electrical communication paths.

18 ~~wherein the video output system is selectively coupled to a video graphics~~  
19 ~~processor as another video signal source, and~~

20 ~~wherein the video output system is selectively coupled to a video input system as~~  
21 ~~another video signal source.~~

22  
23 24. (Original) The method according to claim 23, further comprising:

24 inserting ancillary data into the post-processed video signal prior to converting the  
25 post-processed video signal.

1  
2 25. (Original) The method according to claim 23, further comprising:  
3 generator locking the received video signal.  
4

5 26. (Original) The method according to claim 23 wherein the video output module  
6 includes a generator locking device.  
7

8 27. (Original) The method according to claim 23 wherein the received video  
9 signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded video  
10 signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.  
11

12 28. (Cancelled)  
13

14 29. (Cancelled)  
15

16 30. (Cancelled)  
17

18 31. (Original) The method according to claim 23 wherein the formatted video  
19 signal is VS, wherein VS is an analog composite video signal, an analog component  
20 video signal, a serial digital composite video signal, a serial digital component video  
21 signal, a parallel digital composite video signal, or a parallel digital component video  
22 signal.  
23

24 32. (Original) The method according to claim 23 wherein the process of  
25 post-processing includes region of interest selection.

1  
2 33. (Original) The method according to claim 23 wherein the process of  
3 post-processing includes frame rate matching.  
4

5 34. (Original) The method according to claim 23 wherein the process of  
6 post-processing includes spatial adaptation.  
7

8 35. (Original) The method according to claim 34 wherein the process of spatial  
9 adaptation includes scaling.  
10

11 36. (Original) The method according to claim 34 wherein the process of spatial  
12 adaptation includes picture framing.  
13

14 37. (Original) The method according to claim 36 wherein the process of picture  
15 framing includes letter boxing.  
16

17 38. (Original) The method according to claim 23 wherein the process of  
18 post-processing includes changing the sample rate of the video signal being  
19 post-processed.  
20

21 39. (Original) The method according to claim 23 wherein the process of  
22 post-processing includes gamma removal.  
23

24 40. (Original) The method according to claim 23 wherein the process of  
25 post-processing includes gamma insertion.

1  
2 41. (Original) The method according to claim 23 wherein the process of  
3 post-processing includes color space conversion.  
4

5 42. (Original) The method according to claim 23 wherein the process of  
6 post-processing includes changing frames of video data into interleaved fields of video  
7 data.  
8

9 43. (Original) The method according to claim 23 wherein the process of  
10 post-processing includes addressing on a frame-by-frame basis the video signal being  
11 post-processed.  
12

13 44. (Currently Amended) A video output system for producing video signals  
14 within a video graphics workstation, the video output system comprising:  
15

16 means for receiving a video signal forwarded from a video signal source within  
17 the video graphics workstation;  
18

19 means for post-processing the received video signal through a video pipeline,  
20 producing a post-processed video signal; and  
21

22 means for converting the post-processed video signal, producing a formatted  
23 video signal,  
24

25 wherein the video output system is ~~selectively coupled to~~ configured to receive  
the received video signal from a storage medium as one video signal source, a video  
graphics processor, and a video input system, wherein the video output system is  
communicatively coupled to the storage medium, the graphics processor, and the video  
input system by electrical communication paths.



1       ~~wherein the video output system is selectively coupled to a video graphics~~  
2 ~~processor as another video signal source, and~~

3       ~~wherein the video output system is selectively coupled to a video input system as~~  
4 ~~another video signal source.~~

5  
6       45. (Original) The system according to claim 44, further comprising:  
7       means for inserting ancillary data into the post-processed video signal prior to  
8 converting the post-processed video signal.

9  
10       46. (Previously added) The video output system according to claim 1 wherein the  
11 receiver and the video pipeline are implemented as an integrated video processing  
12 module, and wherein the video output module is detachably coupled to the video  
13 processing module.

14  
15       47. (Previously Added) The video output system according to claim 46 wherein  
16 the video output module is a daughterboard module that couples to the video processing  
17 module.

18  
19       48. (Previously Added) The video output system according to claim 46 wherein  
20 the video output module includes a processor that is configured to inform the video  
21 processing module of its configuration.

22  
23       49. (Previously Added) The method according to claim 23 wherein the receiver  
24 and the video pipeline are implemented as an integrated video processing module, and  
25

1 wherein the method further includes detachably coupling the video output module to the  
2 video processing module.

3  
4 50. (Previously Added) The method according to claim 49 wherein the video  
5 output module is a daughterboard module that couples to the video processing module.

6  
7 51. (Previously Added) The method according to claim 49 wherein the video  
8 output module includes a processor, and wherein the processor informs the video  
9 processing module of its configuration.

10  
11 52. (Currently Amended) A video output system for producing video signals, the  
12 video output system being selectively communicatively coupled by electrical  
13 communication paths to a video graphics processor, a video signal input system, and a  
14 storage medium for storing data in electrical form, the video output system comprising:

15 a receiver for receiving a video signal;

16 a video pipeline for post-processing the received video signal, the video pipeline  
17 producing a post-processed video signal, wherein the video pipeline is configured to  
18 perform at least one function selected from the following functions:

19 region of interest selection;

20 frame rate matching;

21 spatial adaptation;

22 changing the sample rate of the video signal being post processed;

23 gamma removal;

24 gamma insertion;

25 color space conversion;

1 changing frames of video data into interleaved fields of video data; and  
2 addressing on a frame by frame basis the video signal being post  
3 processed; and

4 a video output module for converting the post-processed video signal, the video  
5 output module producing a formatted video signal,

6 wherein the video output system is configured to receive the received video signal  
7 is selectively forwarded from [[:]] the storage medium, the video graphics processor, and  
8 the video input system.

9 ~~the storage medium, when the receiver of the video output system is~~  
10 ~~coupled to the storage medium;~~

11 ~~the video graphics processor, when the receiver of the video output system~~  
12 ~~is coupled to the storage medium; and~~

13 ~~the video signal input system, when the receiver of the video output~~  
14 ~~system is coupled to video signal input system.~~

15  
16 53. (Currently Amended) The video output system according to claim 52 wherein  
17 the video pipeline is configured to perform plural of said functions ~~selected from the~~  
18 ~~following functions:~~

19 ~~region of interest selection;~~

20 ~~frame rate matching;~~

21 ~~spatial adaptation;~~

22 ~~changing the sample rate of the video signal being post processed;~~

23 ~~gamma removal;~~

24 ~~gamma insertion;~~

25 ~~color space conversion;~~

1                    ~~changing frames of video data into interleaved fields of video data; and~~  
2                    ~~addressing on a frame by frame basis the video signal being post~~  
3                    ~~processed.~~

4  
5                    54. (Previously Added) The video output system according to claim 53 wherein  
6                    the video pipeline includes functionality for performing each said function.

7  
8                    55. (Currently Amended) The video output system according to claim ~~[[53]]~~ 51  
9                    wherein the video output module further comprises at least one of:

10                    an ancillary data injector, the injector inserting ancillary data into the  
11                    post-processed video signal; and  
12                    a generator locking device.

13  
14                    56. (Previously Added) The video output system according to claim 54 wherein  
15                    the video output module further comprises:

16                    an ancillary data injector, the injector inserting ancillary data into the  
17                    post-processed video signal; and  
18                    a generator locking device.

19  
20                    57. (Currently Amended) A method for producing video signals using a video  
21                    output system, the video output system being ~~selectively~~ coupled by electrical  
22                    communication paths to a video graphics processor, a video signal input system, and a  
23                    storage medium for storing data in electrical form, the method comprising:

24                    receiving a video signal in a receiver of the video output system;  
25

1 post-processing the received video signal through a video pipeline of the video  
2 output system, producing a post-processed video signal, wherein the video pipeline is  
3 configured to perform at least one function selected from the following functions:

4 region of interest selection;

5 frame rate matching;

6 spatial adaptation;

7 changing the sample rate of the video signal being post-processed;

8 gamma removal;

9 gamma insertion;

10 color space conversion;

11 changing frames of video data into interleaved fields of video data; and

12 addressing on a frame-by-frame basis the video signal being  
13 post-processed; and

14 converting the post-processed video signal in a video output module of the video  
15 output system, producing a formatted video signal,

16 wherein the video output system is configured to receive the received video signal  
17 is selectively forwarded from [[:]] the storage medium, the video graphics processor, and  
18 the video input system.

19 ~~the storage medium, when the receiver of the video output system is coupled to~~  
20 ~~the storage medium;~~

21 ~~the video graphics processor, when the receiver of the video output system~~  
22 ~~is coupled to the storage medium; and~~

23 ~~the video signal input system, when the receiver of the video output~~  
24 ~~system is coupled to video signal input system.~~

1 58. (Currently Amended) The method according to claim 57 wherein the video  
2 pipeline performs plural of said functions ~~selected from the following functions:~~

3 ~~region of interest selection;~~

4 ~~frame rate matching;~~

5 ~~spatial adaptation;~~

6 ~~changing the sample rate of the video signal being post processed;~~

7 ~~gamma removal;~~

8 ~~gamma insertion;~~

9 ~~color space conversion;~~

10 ~~changing frames of video data into interleaved fields of video data; and~~

11 ~~addressing on a frame by frame basis the video signal being~~  
12 ~~post processed.~~

13  
14 59. (Previously Added) The method according to claim 58 wherein the video  
15 pipeline includes functionality for performing each said function.

16  
17 60. (Currently Amended) The method according to claim ~~[[58]]~~ 57, further  
18 comprising performing at least one of:

19 inserting ancillary data into the post-processed video signal; and

20 generator locking the ~~receive~~ received video signal.

21  
22 61. (Previously Added) The method according to claim 59, further comprising:

23 inserting ancillary data into the post-processed video signal; and

24 generator locking the receive video signal.